

YOR920010247DIV

**IN THE CLAIMS:**

Please cancel claims 1-10 without prejudice or disclaimer.

YOR920010247DIV

1 – 10 (Cancelled).

11. (Original) A method of manufacturing an integrated circuit chip structure comprising:  
supplying an integrated circuit chip; and  
patterning a conductive polymer on an exterior of said integrated circuit chip,  
wherein said patterning produces passive devices.
12. (Original) The method in claim 11, wherein said passive devices comprise RF devices.
13. (Original) The method in claim 11, wherein said passive devices comprise at least one of resistors, capacitors, and inductors.
14. (Original) The method in claim 13, wherein said resistors comprise serpentine resistors.
15. (Original) The method in claim 13, wherein said capacitors comprise interdigitated capacitors.
16. (Original) A method of manufacturing an integrated circuit chip structure comprising:  
patterning a conductive polymer on a substrate; and  
bonding said patterned conductive polymer to an integrated circuit chip,  
wherein said patterning produces passive devices.

YOR920010247DIV

17. (Original) The method in claim 16, wherein said passive devices comprise RF devices.
18. (Original) The method in claim 16, wherein, said passive devices comprise at least one of resistors, capacitors, and inductors.
19. (Original) The method in claim 18, wherein said resistors comprise serpentine resistors.
20. (Original) The method in claim 18, wherein said capacitors comprise interdigitated capacitors.